MP03XXX360

Dual Thyristor, Thyristor/Diode Module

Replaces June 2001 version, DS4484-6.1

DS4484-7.0 July 2002

FEATURES

- Dual Device Module
- Electrically Isolated Package
- Pressure Contact Construction
- International Standard Footprint
- Alumina (Non Toxic) Isolation Medium

APPLICATIONS

- Motor Control
- Controlled Rectifier Bridges
- Heater Control
- AC Phase Control

VOLTAGE RATINGS

Type Number	Repetitive Peak Voltages V_{DRM} V_{RRM} V	Conditions
MP03XXX360-12	1200	T_{vj} = 0˚ to 130˚C,
MP03XXX360-10	1000	I_{DRM} = I_{RRM} = 50mA V_{DSM} = V_{RSM} =
MP03XXX360-08	800	V_{DRM} = V_{RRM} + 100V respectively

Lower voltage grades available.

ORDERING INFORMATION

Order As:

MP03HBT360-12 or **MP03HBT360-10** or **MP03HBT360-08**
MP03HBN360-12 or **MP03HBN360-10** or **MP03HBN360-08**
MP03HBP360-12 or **MP03HBP360-10** or **MP03HBP360-08**

Note: When ordering, please use the complete part number.

KEY PARAMETERS

V_{DRM}	**1200V**
$I_{T(AV)}$	**352A**
$I_{TSM(per\ arm)}$	**10600A**
V_{isol}	**3000V**

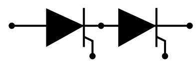
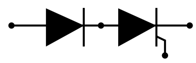
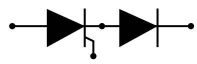
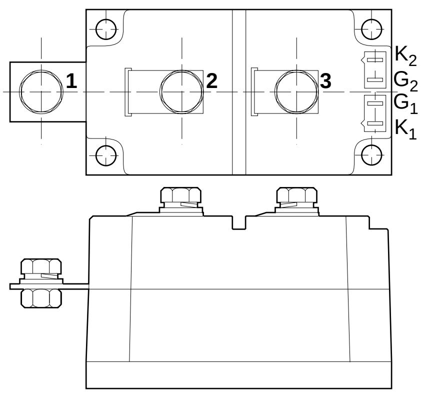
Code	Circuit
HBT	
HBP	
HBN	

Fig.1 Circuit diagrams



Outline type code: MP03

Fig. 2 Electrical connections - (not to scale)

ABSOLUTE MAXIMUM RATINGS - PER ARM

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

Symbol	Parameter	Test Conditions		Max.	Units
$I_{T(AV)}$	Mean on-state current	Half wave resistive load	T_{case} = 75˚C	352	A
			T_{case} = 85˚C	306	A
$I_{T(RMS}$	RMS value	T_{case} = 75˚C		553	A
I_{TSM}	Surge (non-repetitive) on-current	10ms half sine, T_j = 130˚C		10.6	kA
I^2t	I^2t for fusing	V_R = 0		560 x 10^3	A^2s
I_{TSM}	Surge (non-repetitive) on-current	10ms half sine, T_j = 130˚C		8.5	kA
I^2t	I^2t for fusing	V_R = 50% V_{DRM}		360 x 10^3	A^2s
V_{isol}	Isolation voltage	Commoned terminals to base plate. AC RMS, 1 min, 50Hz		3000	V

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
$R_{th(j-c)}$	Thermal resistance - junction to case	dc	-	0.105	˚C/kW
	(per thyristor or diode)	Half wave	-	0.115	˚C/kW
		3 Phase	-	0.12	˚C/kW
$R_{th(c-hs)}$	Thermal resistance - case to heatsink (per thyristor or diode)	Mounting torque = 5Nm with mounting compound	-	0.05	˚C/kW
T_{vj}	Virtual junction temperature	Reverse (blocking)	-	135	˚C
T_{stg}	Storage temperature range	-	–40	135	˚C
-	Screw torque	Mounting - M5	-	5(44)	Nm (lb.ins)
		Electrical connections - M8	-	9(80)	Nm (lb.ins)
-	Weight (nominal)	-	-	950	g

DYNAMIC CHARACTERISTICS - THYRISTOR

Symbol	Parameter	Test Conditions	Min.	Max.	Units
I_{RRM}/I_{DRM}	Peak reverse and off-state current	At V_{RRM}/V_{DRM}, T_j = 130˚C	-	50	mA
dV/dt	Linear rate of rise of off-state voltage	To 67% V_{DRM}, T_j = 130˚C	-	1000	V/µs
dI/dt	Rate of rise of on-state current	From 67% V_{DRM} to 600A, gate source 10V, 5Ω t_r = 0.5µs, T_j = 130˚C	-	500	A/µs
$V_{T(TO)}$	Threshold voltage	At T_{vj} = 135˚C. See note 1	-	0.75	V
r_T	On-state slope resistance	At T_{vj} = 135˚C. See note 1	-	0.7	mΩ

Note 1: The data given in this datasheet with regard to forward voltage drop is for calculation of the power dissipation in the semiconductor elements only. Forward voltage drops measured at the power terminals of the module will be in excess of these figures due to the impedance of the busbar from the terminal to the semiconductor.

GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
V_{GT}	Gate trigger voltage	V_{DRM} = 5V, T_{case} = 25ºC	3	V
I_{GT}	Gate trigger current	V_{DRM} = 5V, T_{case} = 25ºC	150	mA
V_{GD}	Gate non-trigger voltage	At V_{DRM} T_{case} = 125ºC	0.25	V
V_{FGM}	Peak forward gate voltage	Anode positive with respect to cathode	30	V
V_{FGN}	Peak forward gate voltage	Anode negative with respect to cathode	0.25	V
V_{RGM}	Peak reverse gate voltage	-	5	V
I_{FGM}	Peak forward gate current	Anode positive with respect to cathode	10	A
P_{GM}	Peak gate power	See table fig. 5	100	W
$P_{G(AV)}$	Mean gate power	-	5	W

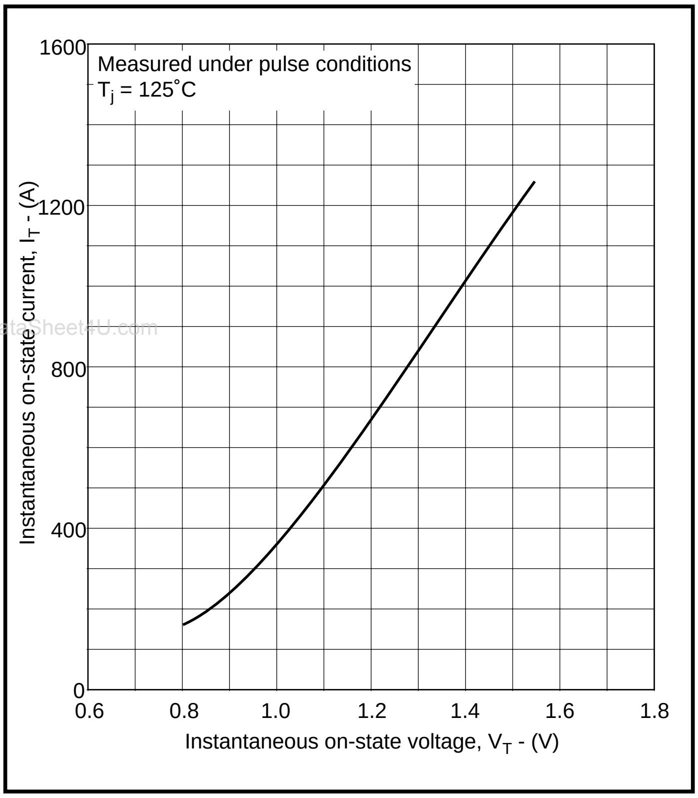


Fig. 3 Maximum (limit) on-state characteristics

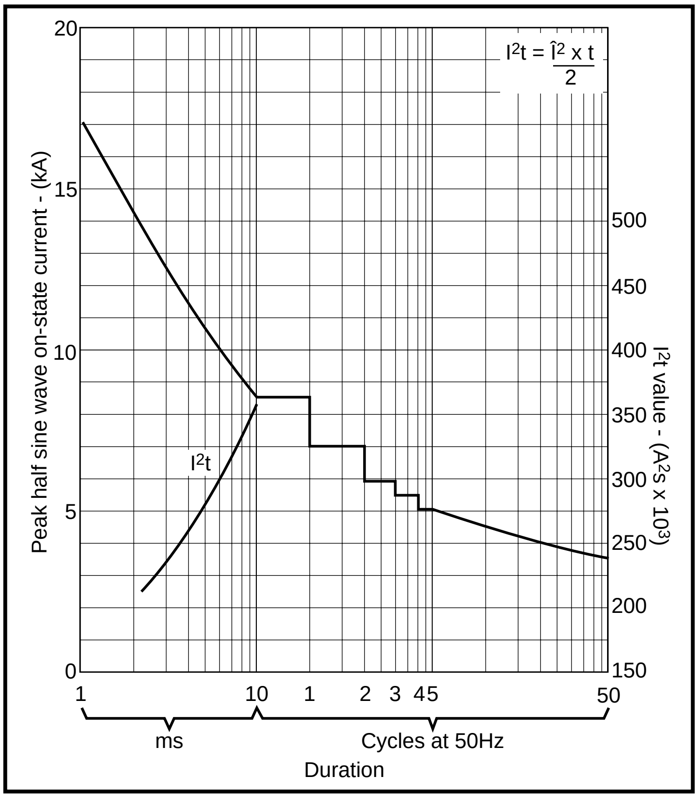
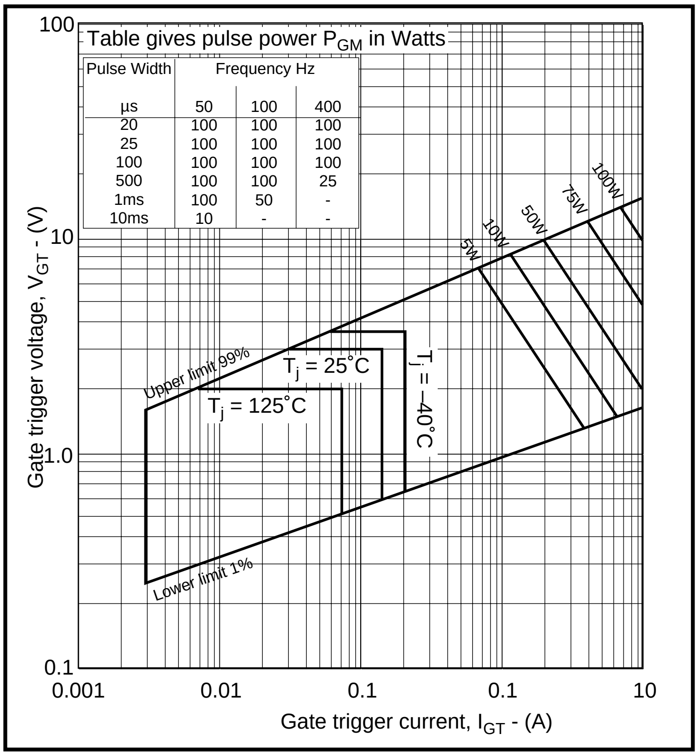


Fig. 4 Surge (non-repetitive) on-state current vs time (Thyristor or diode with 50% V_{RRM} at T_{case} = 130˚C)



Pulse Width	Frequency Hz		
μs	50	100	400
20	100	100	100
25	100	100	100
100	100	100	100
500	100	100	25
1ms	100	50	-
10ms	10	-	-

Fig. 5 Gate characteristics

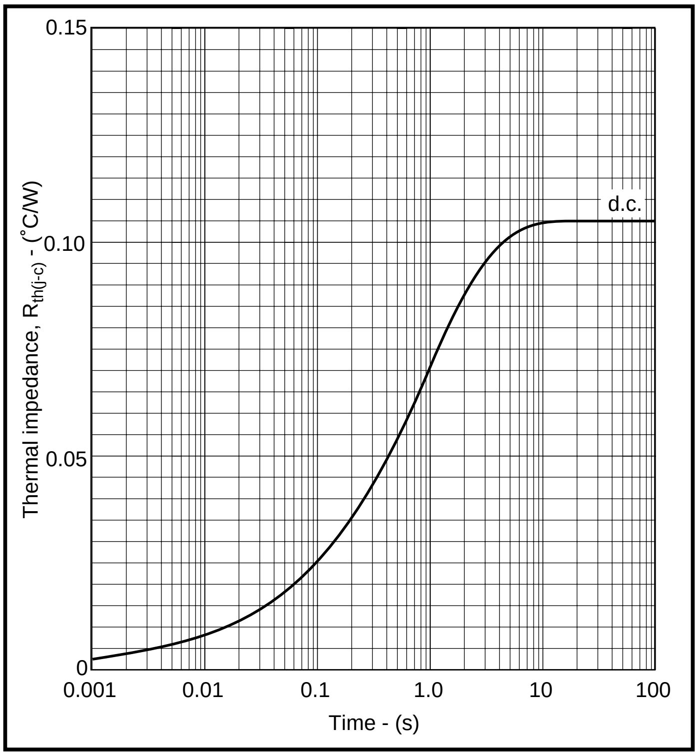


Fig. 6 Transient thermal impedance - dc

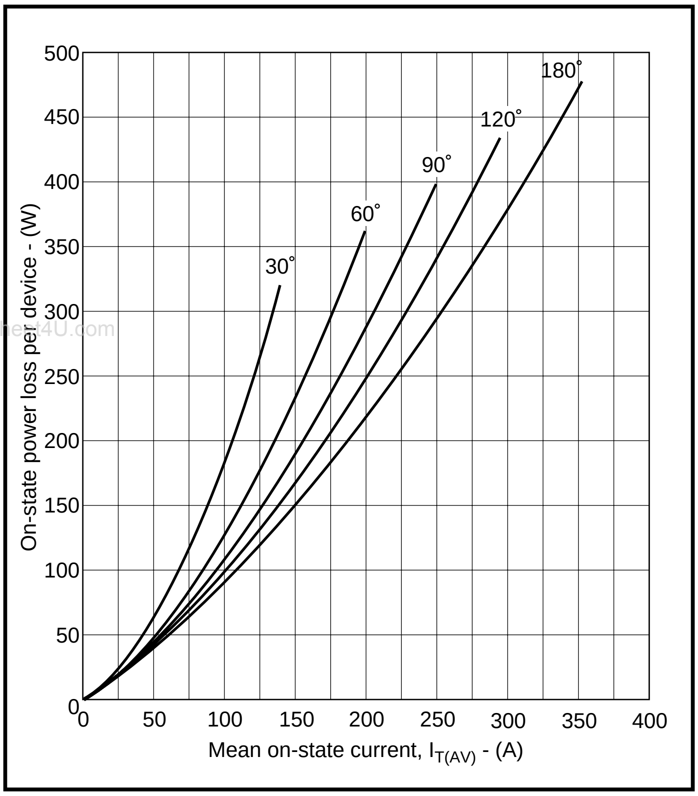


Fig. 7 On-state power loss per arm vs on-state current at specified conduction angles, sine wave 50/60Hz

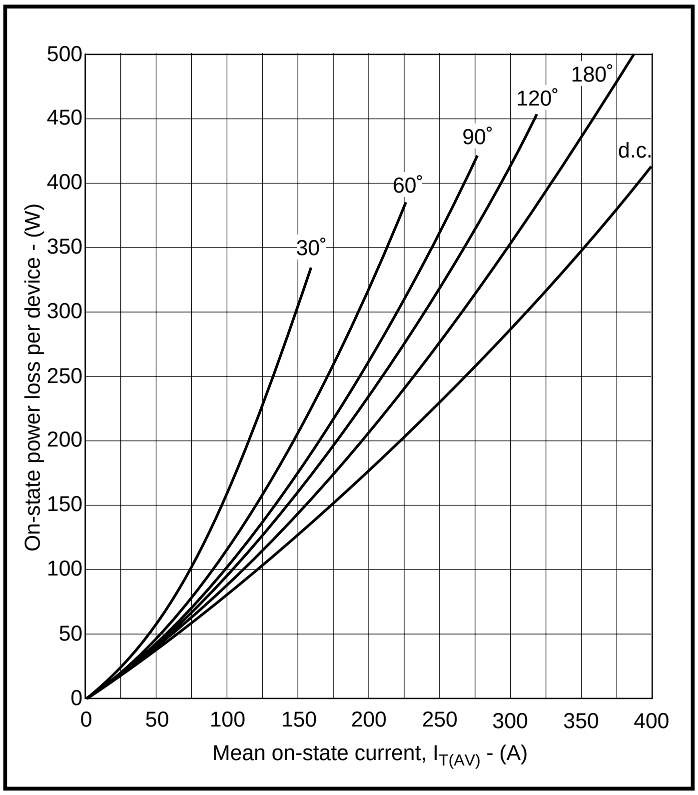


Fig. 8 On-state power loss per arm vs on-state current at specified conduction angles, square wave 50/60Hz

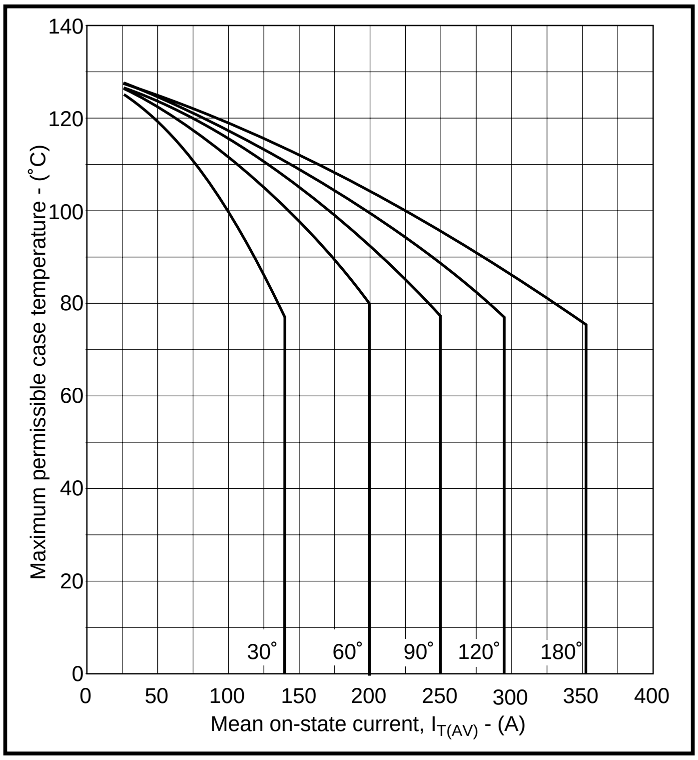


Fig. 9 Maximum permissible case temperature vs on-state current at specified conduction angles, sine wave 50/60Hz

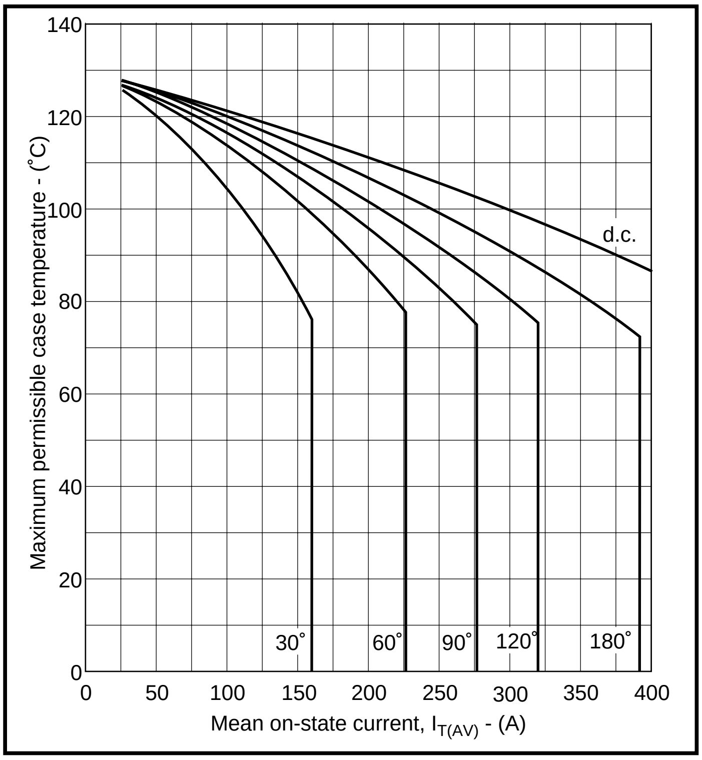


Fig. 10 Maximum permissible case temperature vs on-state current at specified conduction angles, square wave 50/60Hz

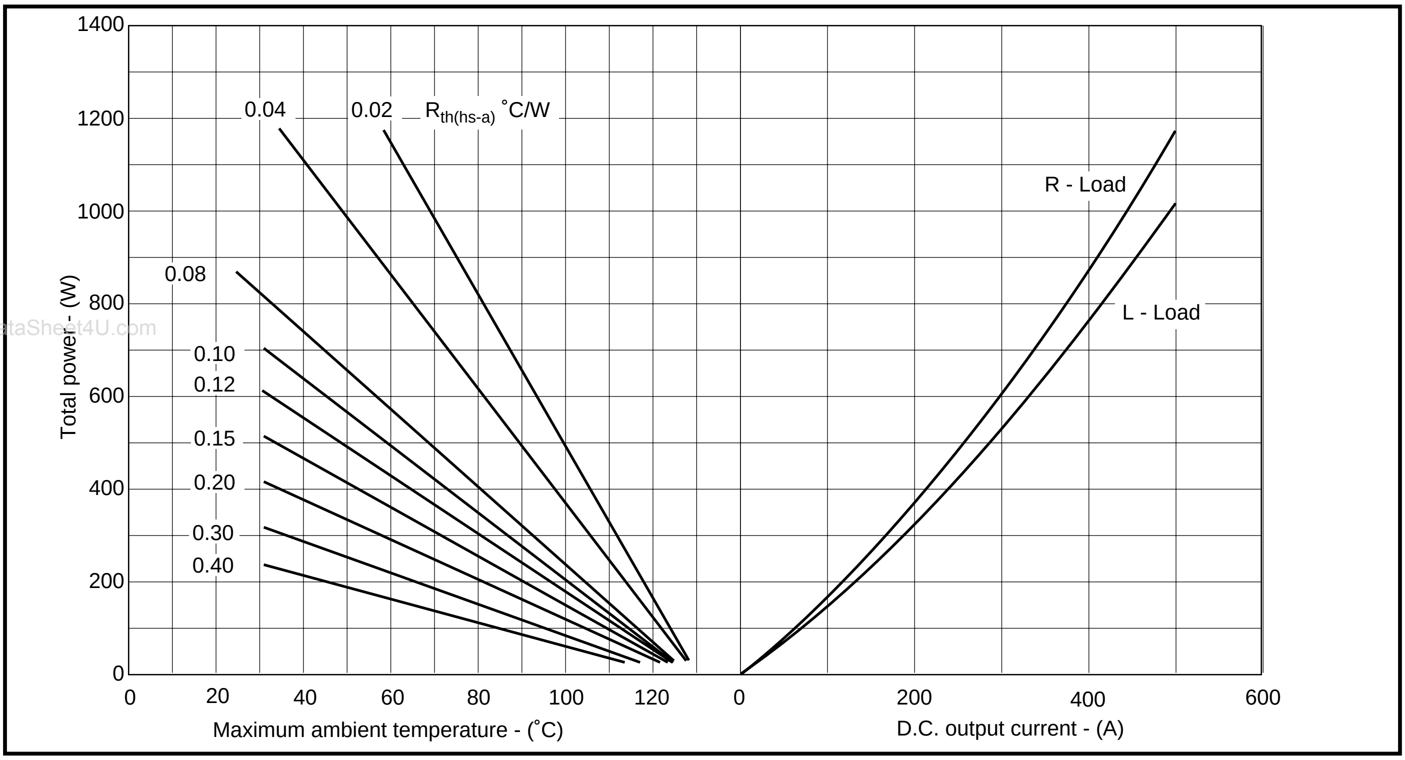


Fig. 11 50/60Hz single phase bridge dc output current vs power loss and maximum permissible ambient temperature for various values of heatsink thermal resistance

(Note: $R_{th(hs-a)}$ values given above are true heatsink thermal resistances to ambient and already account for $R_{th(c-hs)}$ module contact thermal)

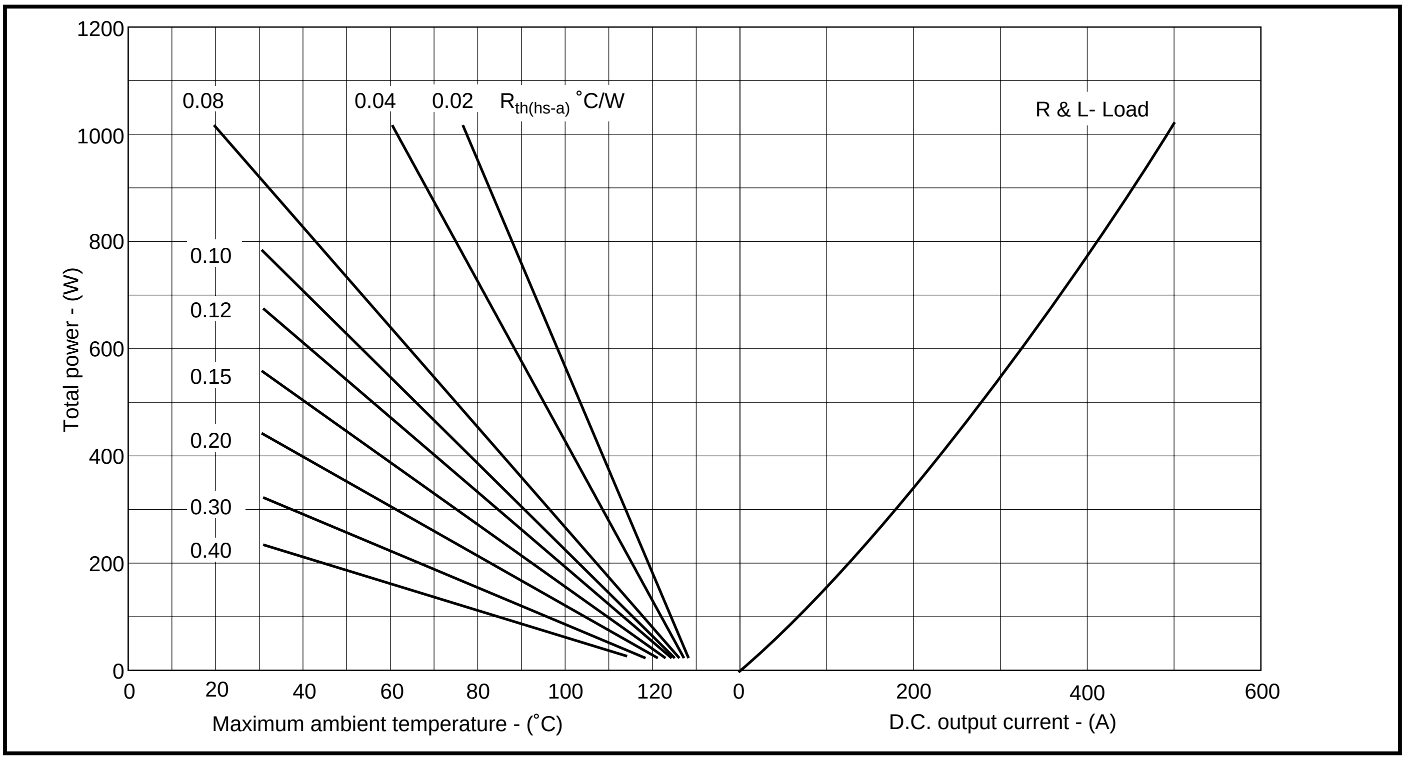
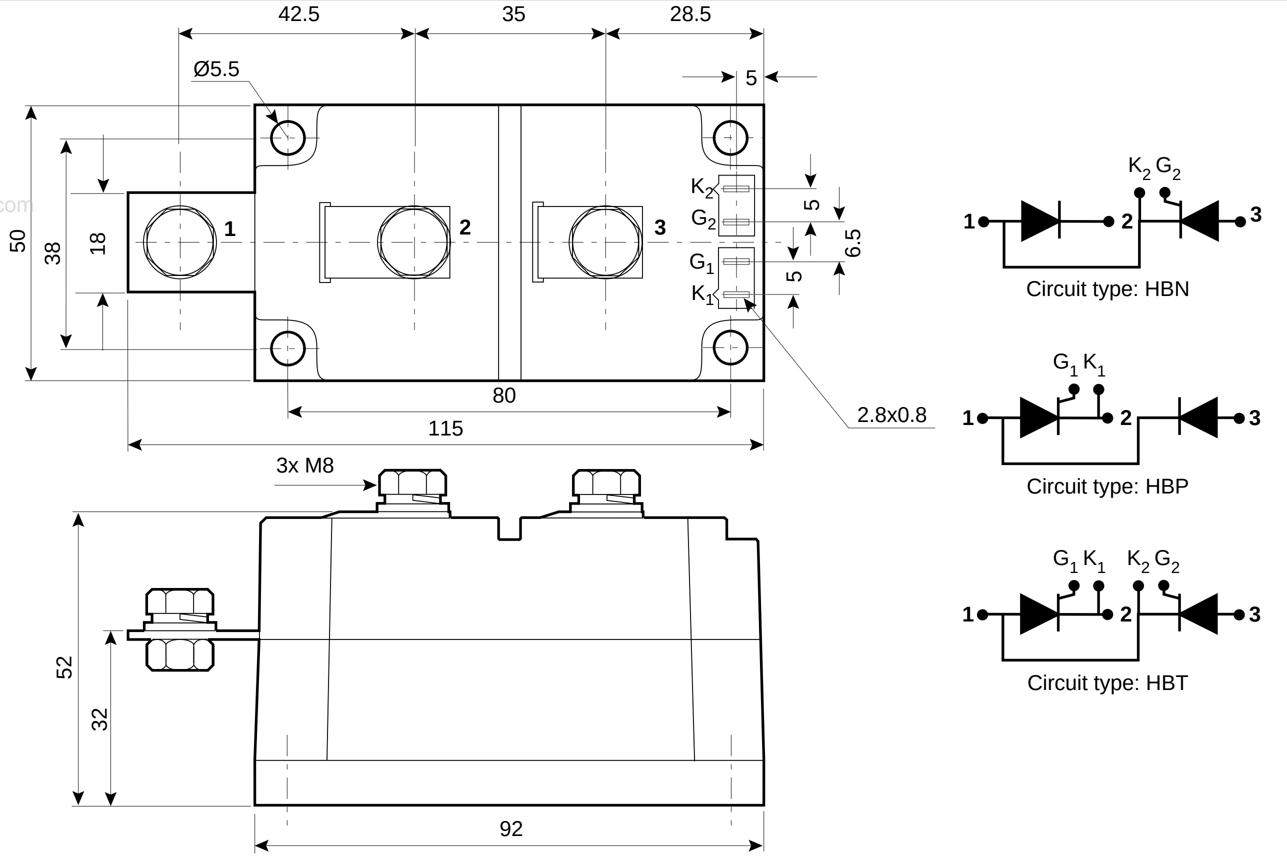


Fig. 12 50/60Hz 3- phase bridge dc output current vs power loss and maximum permissible ambient temperature for various values of heatsink thermal resistance

(Note: $R_{th(hs-a)}$ values given above are true heatsink thermal resistances to ambient and already account for $R_{th(c-hs)}$ module contact thermal)

PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise.
DO NOT SCALE.



Recommended fixings for mounting: M5 socket head cap screws.
Nominal weight: 950g
Auxiliary gate/cathode leads are not supplied but may be purchsed separately.

Module outline type code: MP03

MOUNTING RECOMMENDATIONS

Adequate heatsinking is required to maintain the base temperature at 75°C if full rated current is to be achieved. Power dissipation may be calculated by use of $V_{T(TO)}$ and r_T information in accordance with standard formulae. We can provide assistance with calculations or choice of heatsink if required.

The heatsink surface must be smooth and flat; a surface finish of N6 (32μin) and a flatness within 0.05mm (0.002") are recommended.

Immediately prior to mounting, the heatsink surface should be lightly scrubbed with fine emery, Scotch Brite or a mild chemical etchant and then cleaned with a solvent to remove oxide build up and foreign material. Care should be taken to ensure no foreign particles remain.

An even coating of thermal compound (eg. Unial) should be applied to both the heatsink and module mounting surfaces. This should ideally be 0.05mm (0.002") per surface to ensure optimum thermal performance.

After application of thermal compound, place the module squarely over the mounting holes, (or 'T' slots) in the heatsink. Fit and finger tighten the recommended fixing bolts at each end. Using a torque wrench, continue to tighten the fixing bolts by rotating each bolt in turn no more than 1/4 of a revolution at a time, until the required torque of 6Nm (55lbs.ins) is reached on all bolts at both ends.

It is not acceptable to fully tighten one fixing bolt before starting to tighten the others. Such action may DAMAGE the module.

POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group offers high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

HEATSINKS

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks which have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or Customer Services.

DYNEX SEMICONDUCTOR

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Target Information: This is the most tentative form of information and represents a very preliminary specification. No actual design work on the product has been started.

Preliminary Information: The product is in design and development. The datasheet represents the product as it is understood but details may change.

Advance Information: The product design is complete and final characterisation for volume production is well in hand.

No Annotation: The product parameters are fixed and the product is available to datasheet specification.